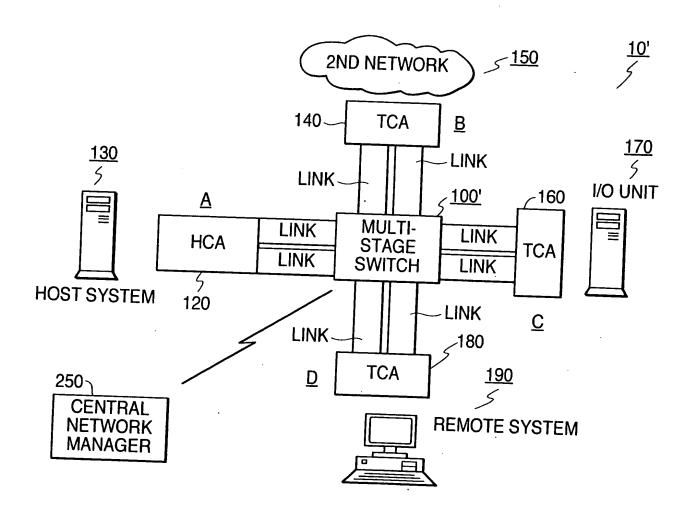
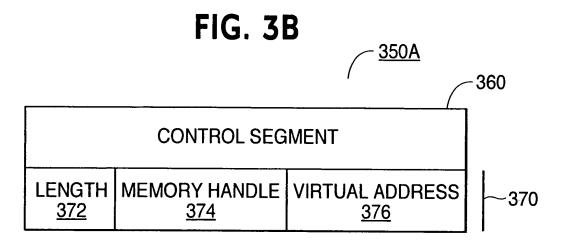
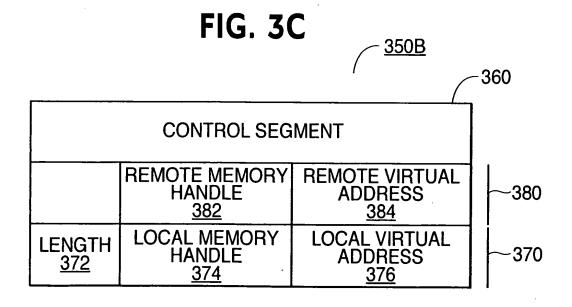
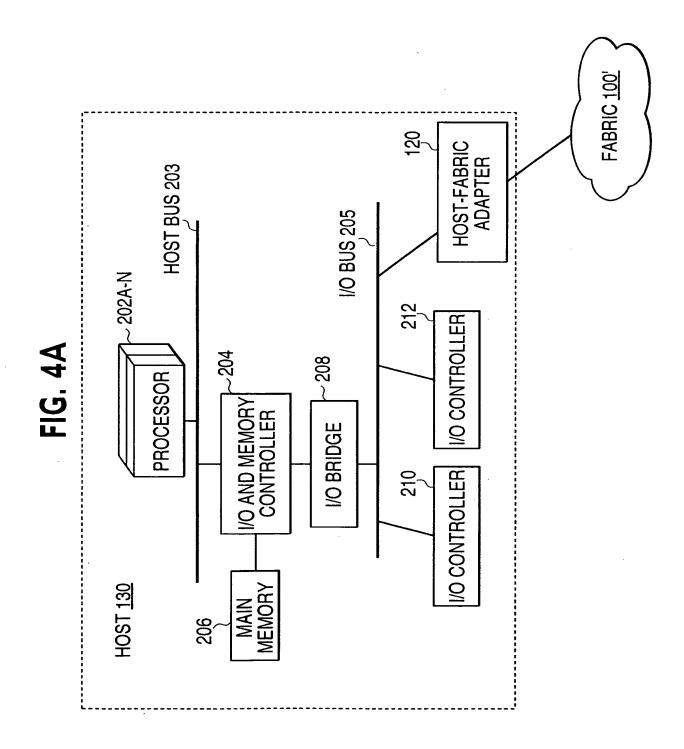


FIG. 2











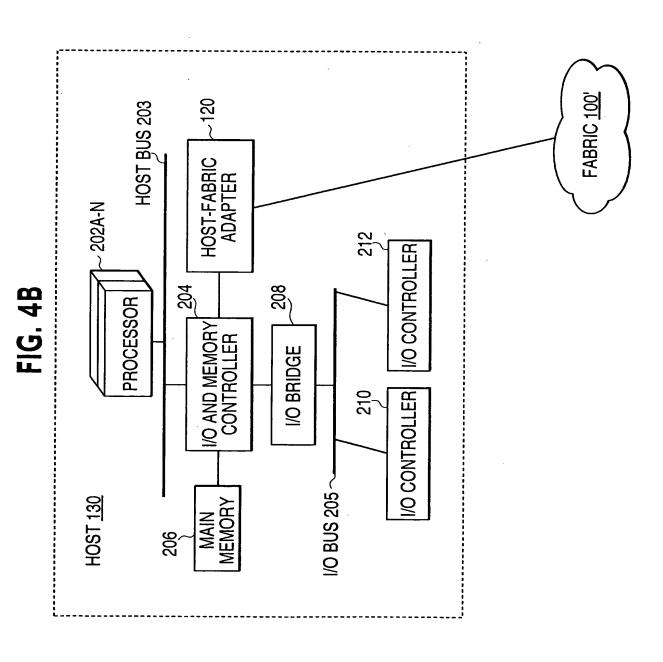
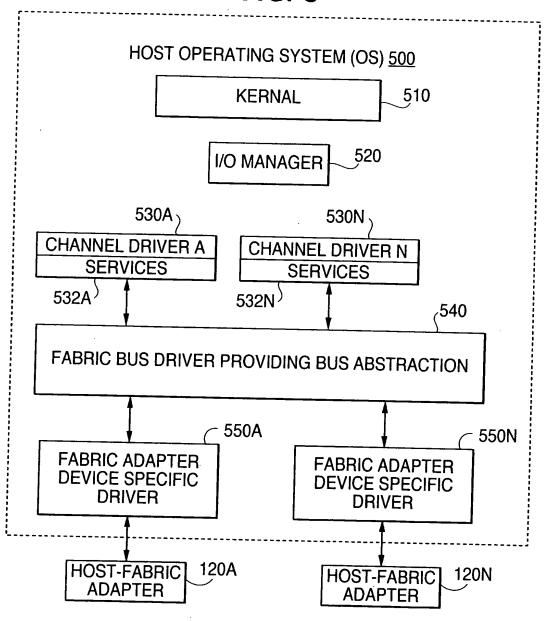


FIG. 5



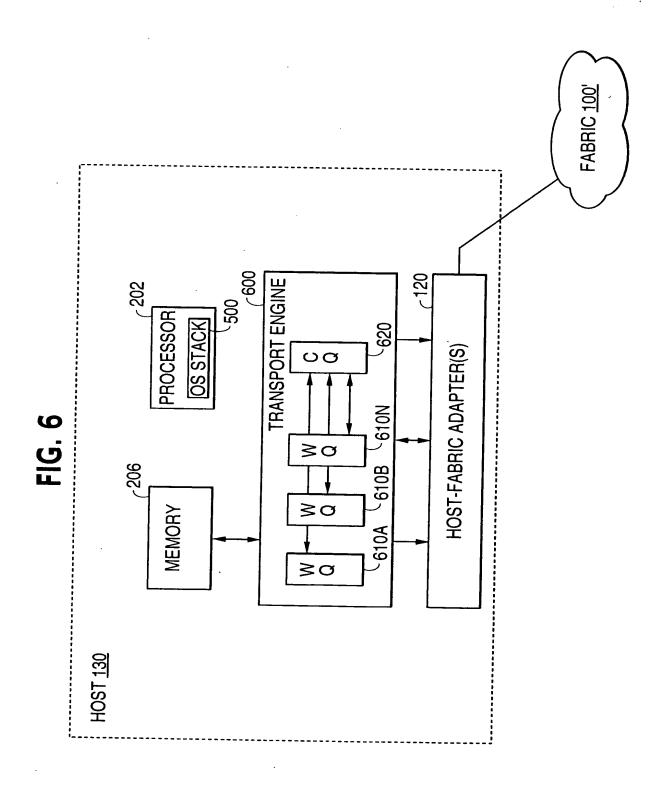


FIG. 7 $\frac{120}{1}$ ₍700 HOST INTERFACE _C714 **ADDRESS ADDRESS** TRANSLATION ⁽712 TRANSLATION **BLOCK** INTERFACE CONNECTION *←*716 VI CONTEXT **MEMORY C710 MICRO** *-*718 **ENGINE** SYSTEM ACCESSIBLE LOCAL BUS CONTEXT INTERFACE CONNECTION _C720 COMPLETION COMPLETION QUEUE/DOOR-QUEUE/ BELL MANAGER DOORBELL **BLOCKS** MANAGER C722CONNECTION INTERFACE FIFO 722A 722B INTERFACE **~730 SERIAL INTERFACE** FABRIC 100'

FIG. 8



